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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/667,911	09/22/2003	Elliot N. Linzer	03-1089 1496.00325	9918
24319	7590	08/25/2006	EXAMINER DESIR, JEAN WICEL	
LSI LOGIC CORPORATION 1621 BARBER LANE MS: D-106 MILPITAS, CA 95035			ART UNIT 2622	PAPER NUMBER

DATE MAILED: 08/25/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/667,911	LINZER, ELLIOT N.	
	<b>Examiner</b>	<b>Art Unit</b>	
	Jean W. Désir	2622	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 5/30/06 (Amendment).

2a)  This action is **FINAL**.                            2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

4)  Claim(s) 1-25 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
5)  Claim(s) \_\_\_\_\_ is/are allowed.  
6)  Claim(s) 1-25 is/are rejected.  
7)  Claim(s) \_\_\_\_\_ is/are objected to.  
8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All    b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_  
4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_ .  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: \_\_\_\_\_

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 102***

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 2, 13, 14, 15 are rejected under 35 U.S.C. 102(e) as being anticipated by Dujmenovic et al (US 7,068,329).

#### **Claim 1:**

Dujmenovic discloses:

An apparatus (Fig. 1) comprising:

“a first circuit configured to receive an encoded video signal at a first input and to present a decoded video signal at a first output”, see Fig. 1 item 114;

“and a second circuit (see Fig. 1 items 116, 118, 120) configured to receive said decoded video signal at a second input and to present (i) a first video output signal (CH1) having a first resolution at a second output and (ii) a second video output signal (CH2) having a second resolution at a third output, wherein said first video output signal (CH1) and said second video output signal (CH2) are generated in response to said decoded video signal (item 114 of Fig. 1)”.

**Claim 2:**

“a decoder circuit configured to generate said decoded video signal in response to said encoded video signal” is disclosed, see Fig. 1 item 114;  
“and a memory circuit configured to store said decoded video signal” is disclosed, see Fig. 1 item 116.

**Claim 13:**

Dujmenovic discloses:

An apparatus (Fig. 1) comprising:

“means for generating a decoded video signal in response to an encoded video signal, wherein said encoded video signal is received at a first input and said decoded video signal is presented at a first output”, see Fig. 1 item 114;

“and means (see Fig. 1 items 116, 118, 120) for generating (i) a first video output signal (CH1) having a first resolution and (ii) a second video output signal (CH2) having a second resolution in response to said decoded video signal (item 114 of Fig. 1), wherein said first video output signal (CH1) is presented at a second output and said second video output signal (CH2) is presented at a third output”.

**Claim 14** is rejected for the same reasons as claim 13.

Claim 15 is rejected for the same reasons as claim 2.

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claims 3-12, 16-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Dujmenovic et al (US 7,068,329) in view of Abe (US 5,504, 535).

Claim 3:

The claimed “a scaler circuit configured to generate a first intermediate signal and a second intermediate signal in response to said decoded video signal” is not explicitly disclosed by Dujmenovic. However, Dujmenovic disclosed that multiple images (video signals) can be displayed simultaneously on a single display device (see Fig 1 item 120, col. 6 lines 8-9); and scaler circuit, as claimed, used in the art for purpose of scaling images (video signals) in order to provide them simultaneously on a display device is notoriously well known in the art (as evidence see Abe at Fig. 1 items 4, 5, 13, col. 7 line 66 to col. 8 line 6, col. 8 lines 52-60); thus, an artisan would be motivated to modify Dujmenovic’s disclosure and implement this existing circuit in order to arrive at the claimed invention; this implementation would advantageously provide multiple images on a display device simultaneously. Therefore, the claimed invention would have been obvious to a person of ordinary skill in the art at the time the invention was made.

Claim 4 is disclosed in view of the above implementation, see Abe at Fig. 1 items 7, 8, 16, 17.

Claim 5 is disclosed, see Abe at col. 10 lines 10-32.

Claims 6-8 are disclosed, see Abe at col. 10 lines 33-65.

Claim 9 is disclosed, see Abe at col. 11 lines 18-29.

Claim 10 is disclosed, see Abe at Fig. 2.

Claim 11 is disclosed, see Abe at col. 7 lines 35-37, col. 8 lines 25-38.

Claim 12 is disclosed, see Abe at Fig. 1 items 4, 5, 13, and the ABSTRACT.

Claims 16, 17 are disclosed in view of the above implementation, see Abe at Fig. 1 items 4, 5, 13, and the ABSTRACT.

Claims 18-24 are rejected for the same reasons as claims 4-10.

Claim 25 is disclosed, see Abe at col. 7 lines 35-37, col. 8 lines 25-38, col. 4 lines 64-67.

### ***Response to Arguments***

5. Applicant's arguments have been fully considered but they are moot in view of the new ground of rejection.

### **Conclusion**

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not

mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jean W. Désir whose telephone number is (571) 272 7344. The examiner can normally be reached on 5/4/9 - First Friday Off.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David L. Ometz can be reached on (571) 272 7593. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JWD  
Aug. 18, 06

  
DAVID OMETZ  
SUPERVISORY PATENT EXAMINER